PAGE 05

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Stephen W. Russell

Art Unit:

2815

Serial No.:

09/938,260

Examiner.

Fenty, J. A.

Filing Date: 08/23/2001

Docket No.: TI-25084

Title:

IMPROVED DIELECTRIC LAYER LINER FOR AN INTEGRATED

CIRCUIT STRUCTURE

Amendment under 37 CFR 1.111

FAX RECEIVED

Assistant Commissioner of Patents Washington, DC 20231

MAR 4 2003

TECHNOLOGY CENTER 2800

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated 11/04/2002. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

In the Claims:

Replace claim 1 with the following claim:

- (amended) An integrated circuit structure, comprising:
 - a plurality of solid state electronic devices;

a plurality of conductive elements electrically coupling the electronic devices:

a dielectric layer positioned between two or more of the conductive elements; and

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To:

Technology Center 2800

Facsimile Number: 703-872-9318

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Jacqueline J. Gamer

Texas instruments incorporated

Facsimile: 972-917-4418 Phone: 214-532-9348

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CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-

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queline J. Garner, Reg. No. 36,144

March 4, 2003

Date

FACSIMILE COVER SHEET

X FACSIMILE COVER SHEET NEW APPLICATION DECLARATION (# Pages) ASSIGNMENT (# Pages) FORMAL DRAWINGS INFORMAL DRAWINGS CONTINUATION APP'N (# Pages) DIVISIONAL APP'N		X AMENDMENT (5 Pages) X EOT 1 mth (3 Pages) NOTICE OF APPEAL (# Pages) APPEAL (# Pages) ISSUE FEE (# Pages) REPLY BRIEF (IN TRIPLICATE) (# Pages)
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		Serial No.: 09/938,260
Russell TITLE OF INVENTION:		Filing Date: 08/23/2001
Improved Dielectric Layer Liner		
TI FILE NO.:		
T1-25084	20-0668	
FAXED: March 4, 2003		
DUE: 2/4/2003		
ATTY/SECY: JJG		Approx 2 Amount of the Control of th

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